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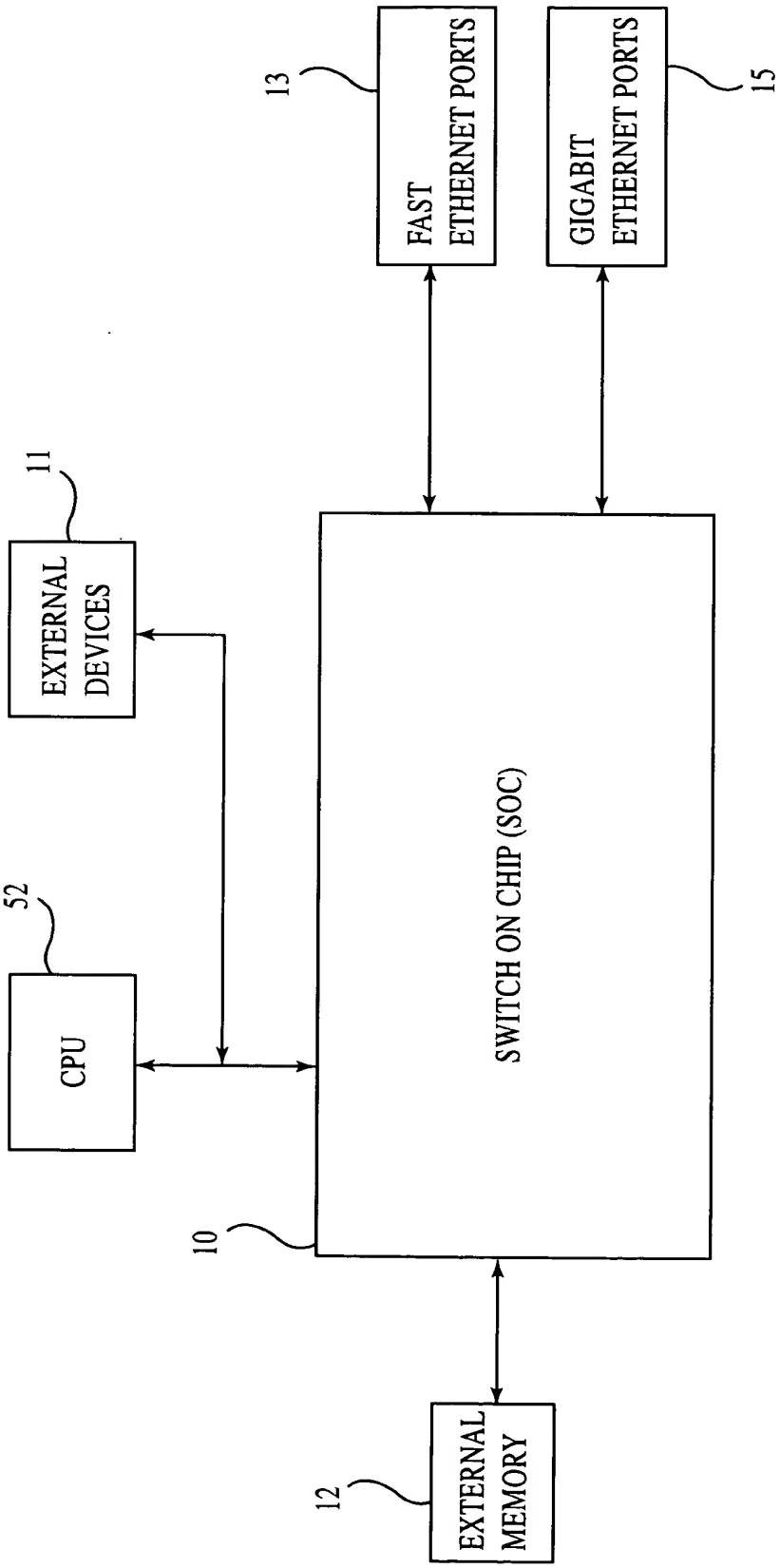
Fig.1



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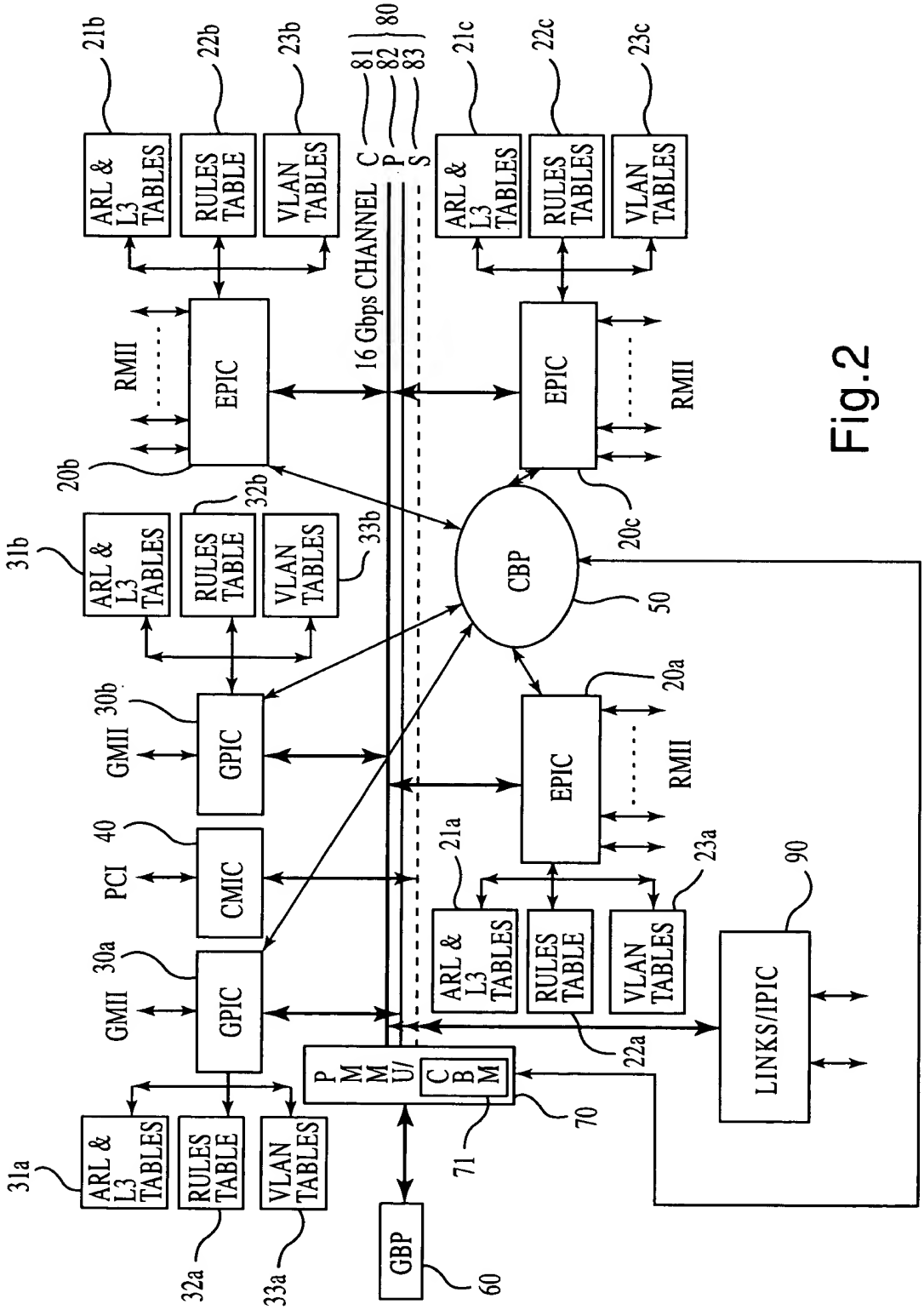

Fig.2

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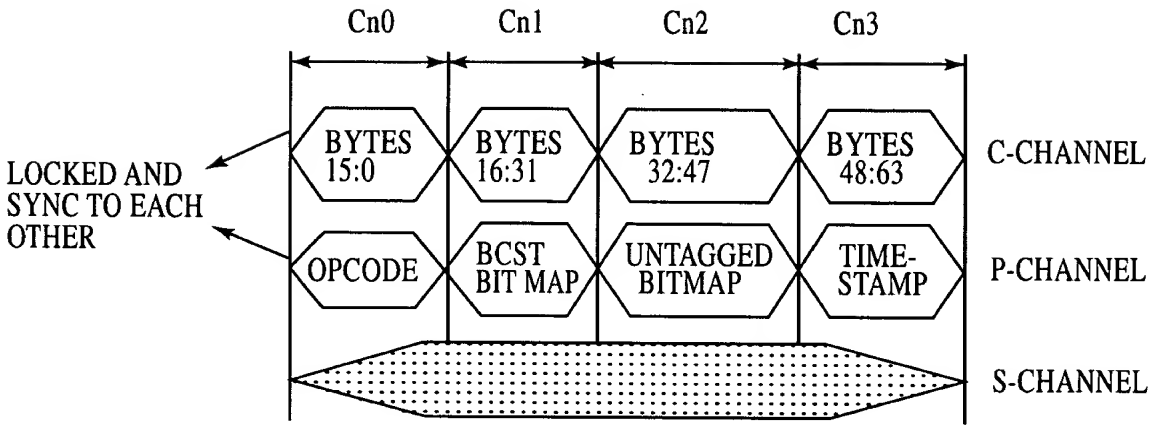
Fig.3



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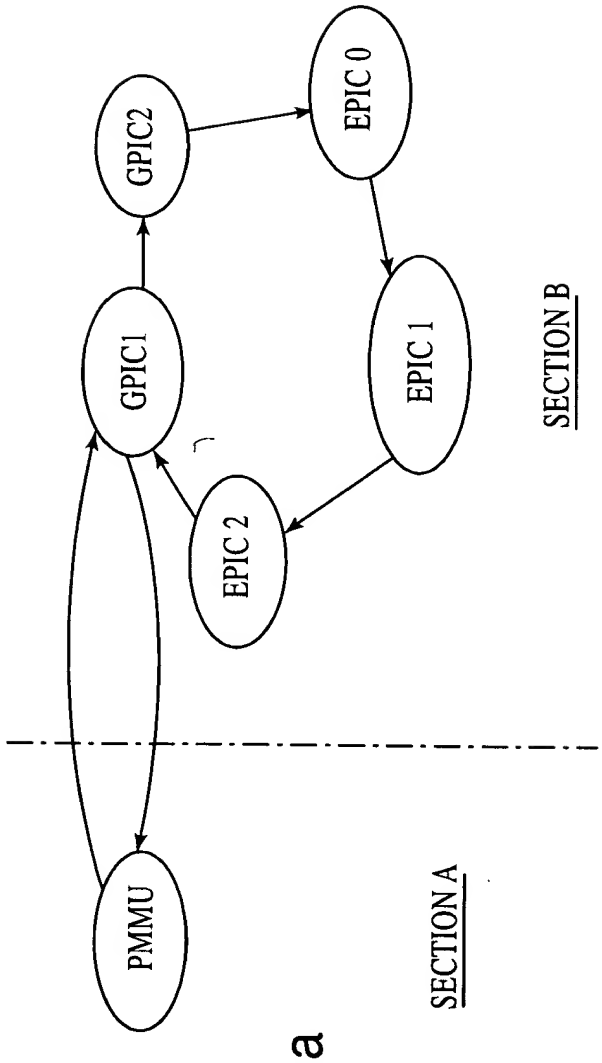

Fig.4a

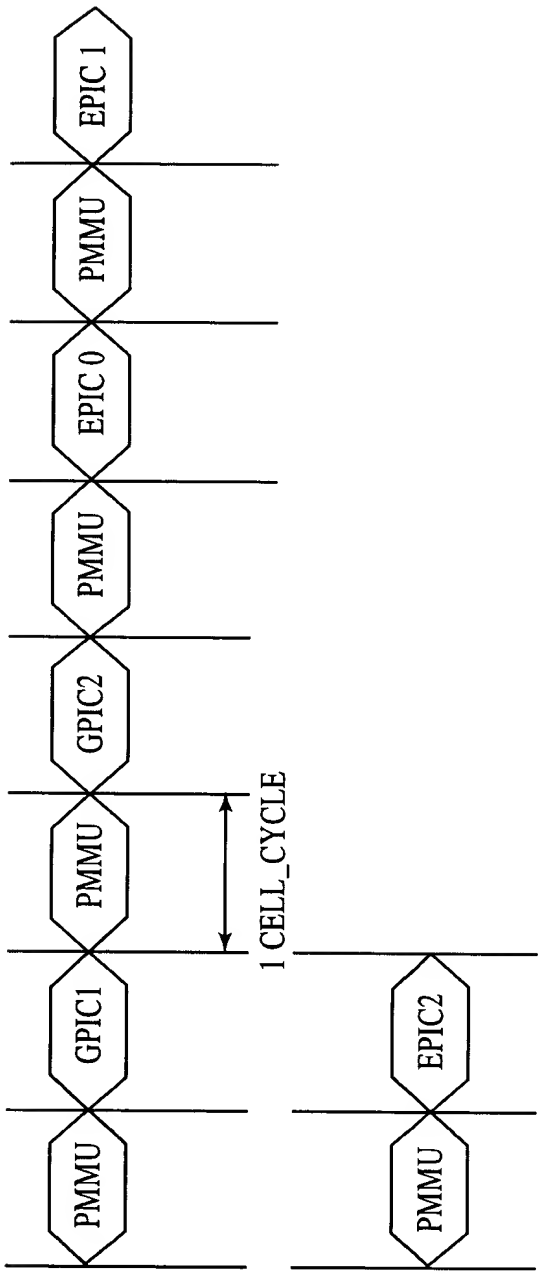

Fig.4b

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Fig.5

PROTOCOL CHANNEL MESSAGES

30	28		26	24	22	20	18	16	14	12		10	8		6	4	2	0
OP CODE	I P	I P X	RESERVED	NXT CELL	SRC DEST PORT			COS	J	S	E	CR C	P	O	LEN			

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
RESERVED	BC/MC PORTBITMAP														

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
U	RES	UNTAGGED PORTBITMAP/SRC PORT NUMBER (BIT0..5)													

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
CPU OPCODES									TIMESTAMP						

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Fig.6

SIDE BAND CHANNEL MESSAGES

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
OPCODE			DEST PORT / DESTINATION DEV ID			SRC PORT			DataLen			E	ECODE	COS	C
ADDRESS															
DATA															

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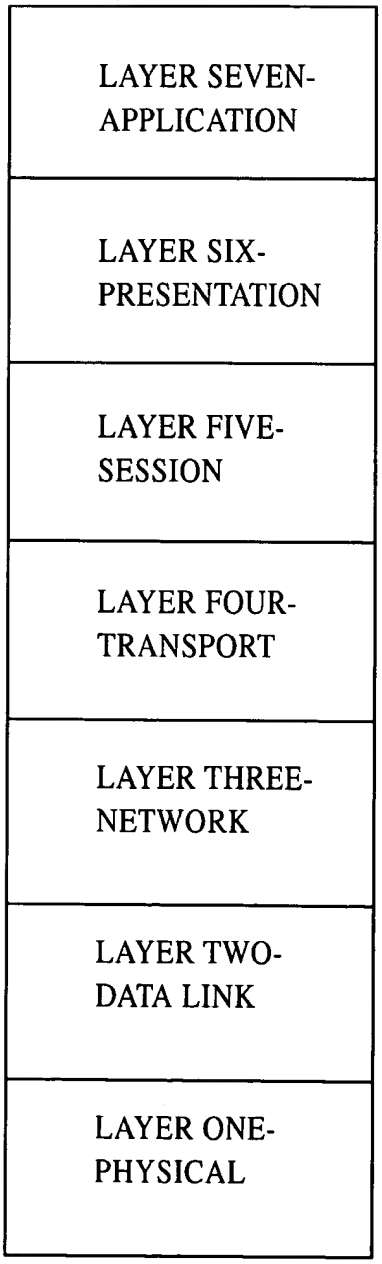
Fig.7
PRIOR ART



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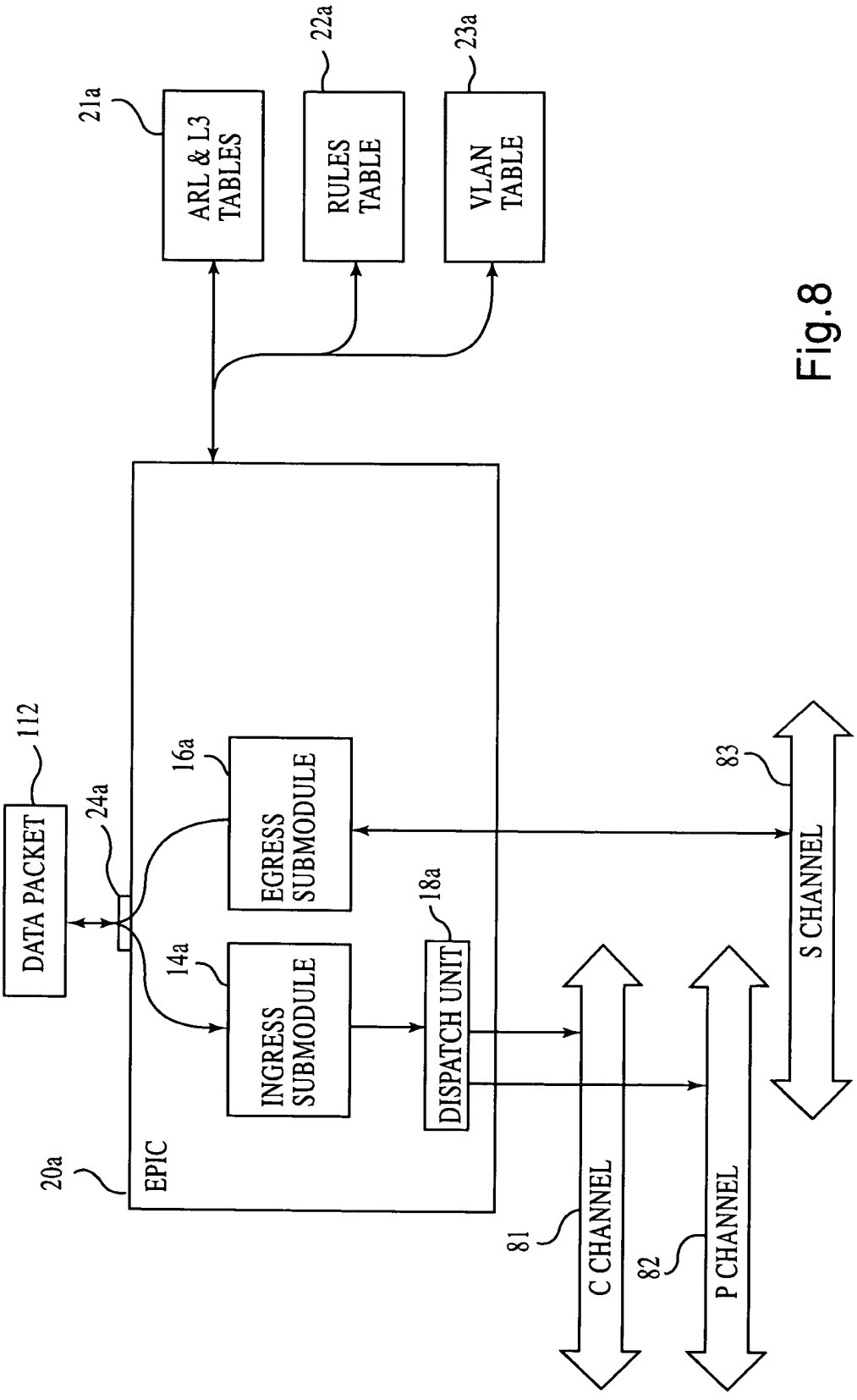

Fig.8

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Fig.9

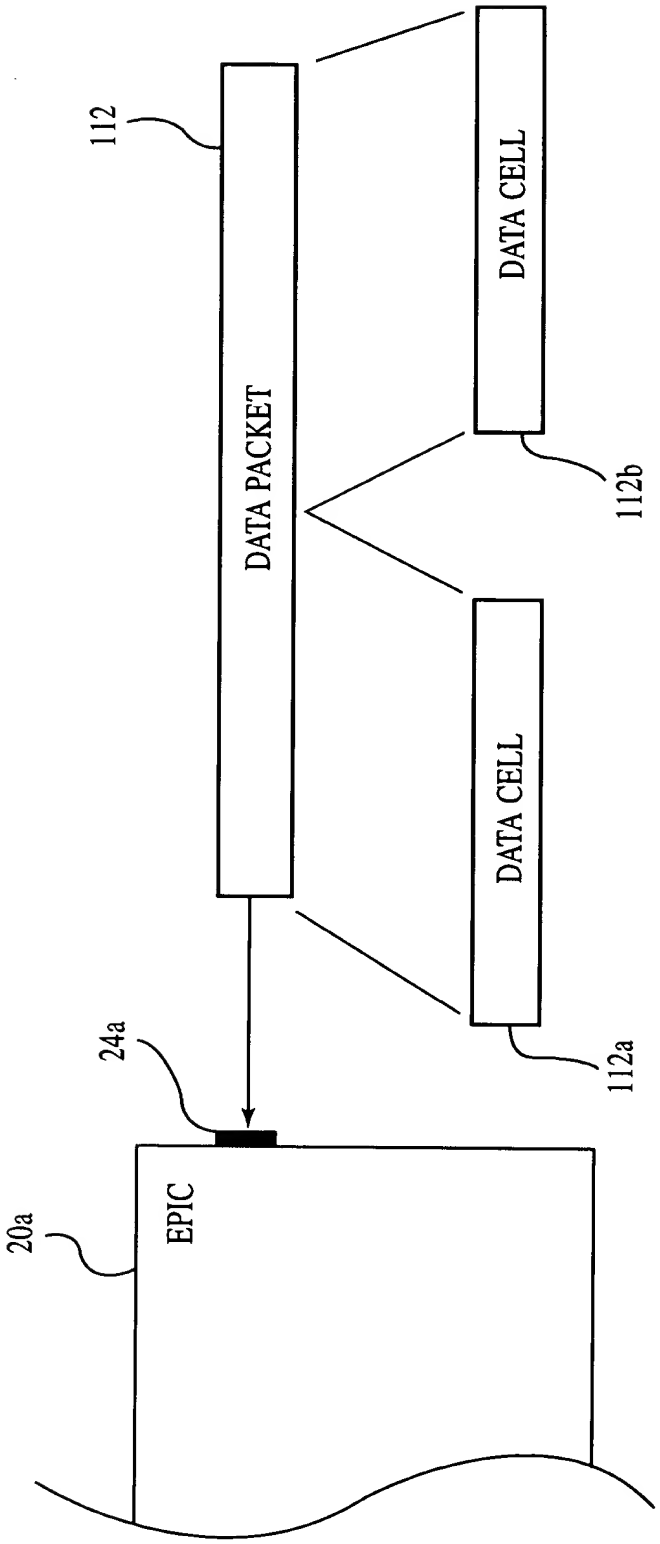

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Fig.10

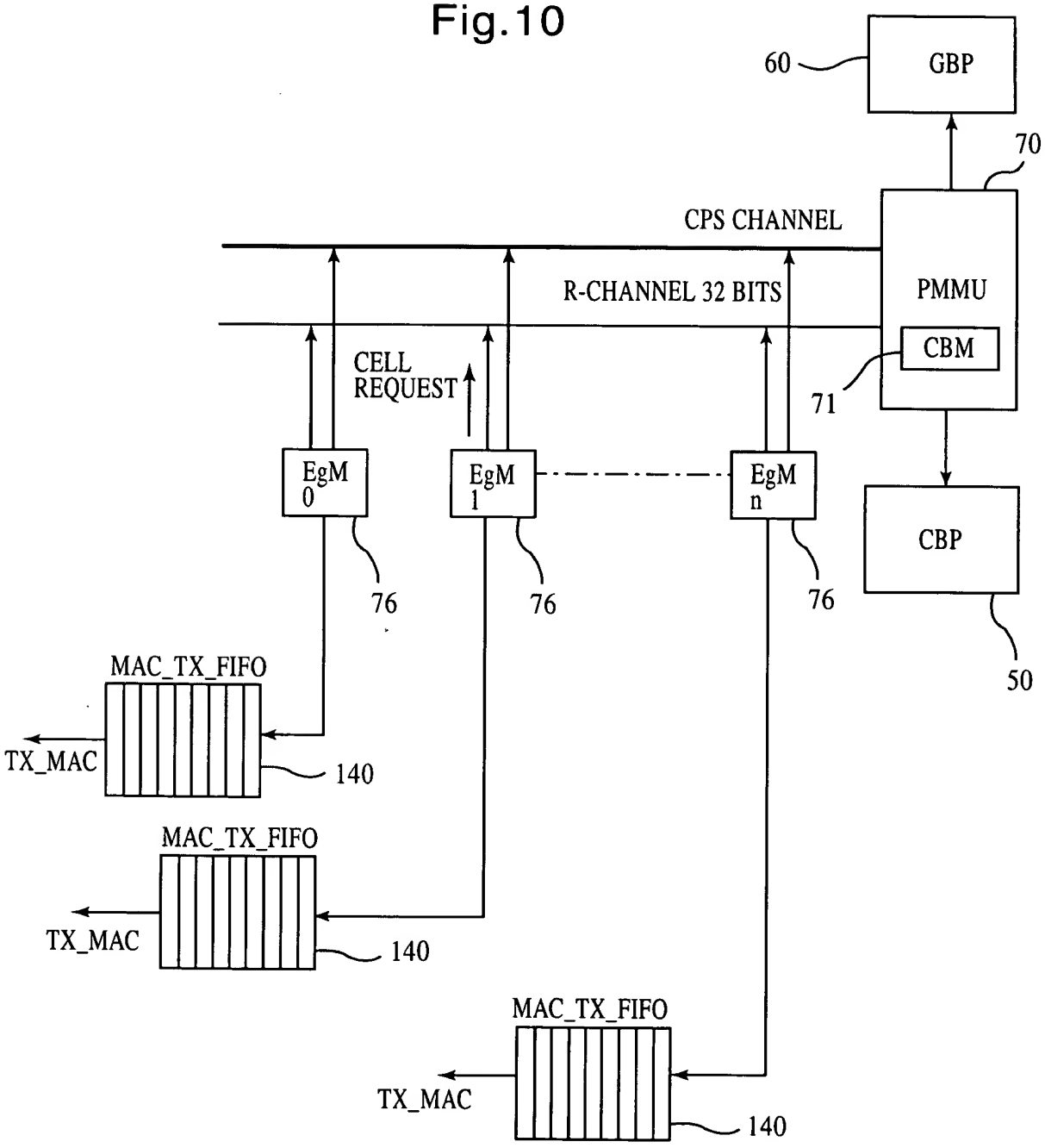

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Fig.11

LINE 0 →	FC | LC | BC/MC | Cpy_cnt (5b) | Cell_length (7b) | CRC (2b) | NC_header (16b) | Src Count (6) | IPX | IP | | Time_Stamp (14b) | O bits (2b) | P | NextCellLen(2b) | CpuOpcode(4b) | Cell_data (0-9B)
LINE 1 →	Cell_data (10-27) Bytes
LINE 2 →	Cell_data (28-45) Bytes
LINE 3 →	Cell_data (46-63) Bytes

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Fig.12

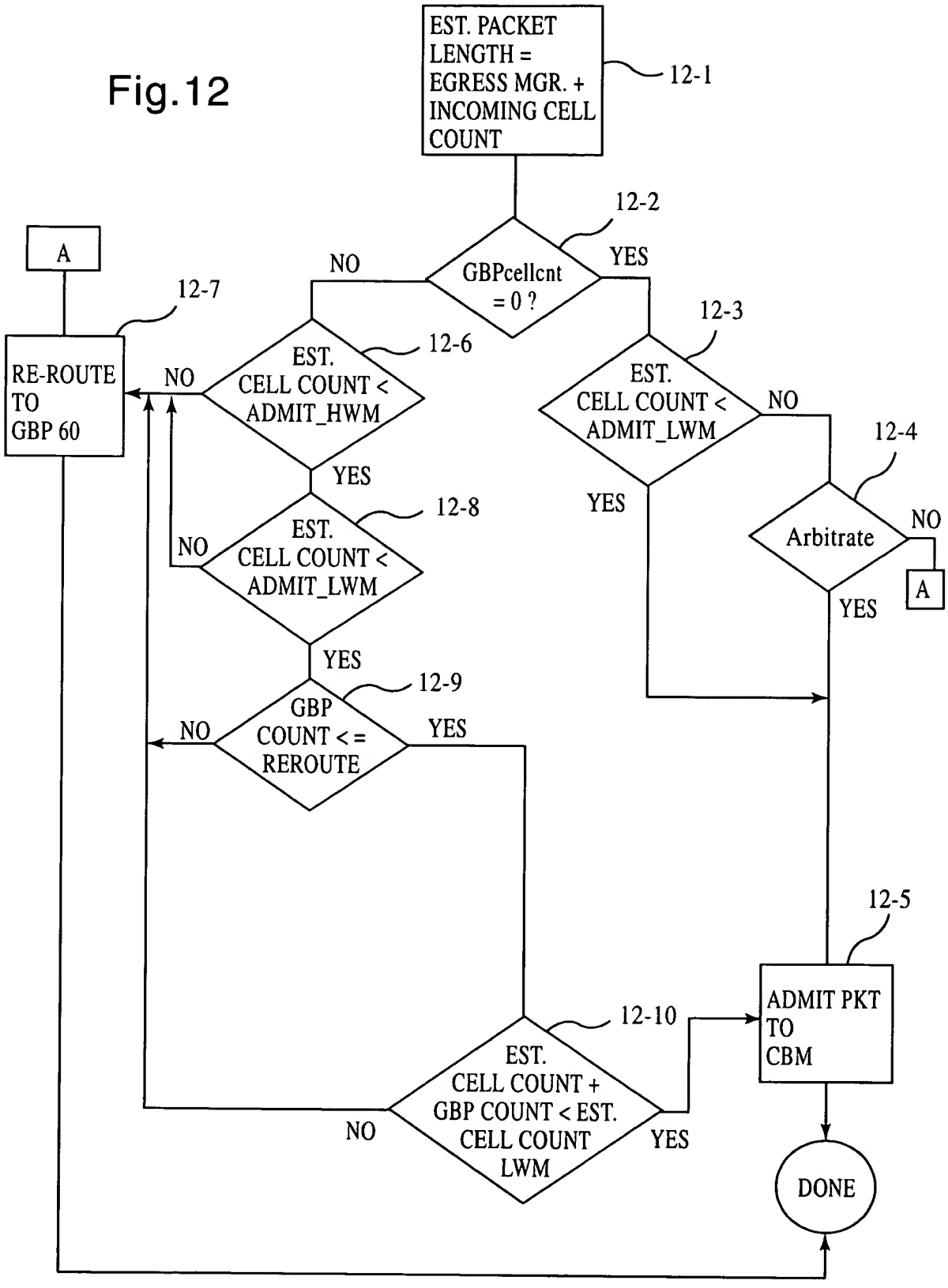

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Fig.13

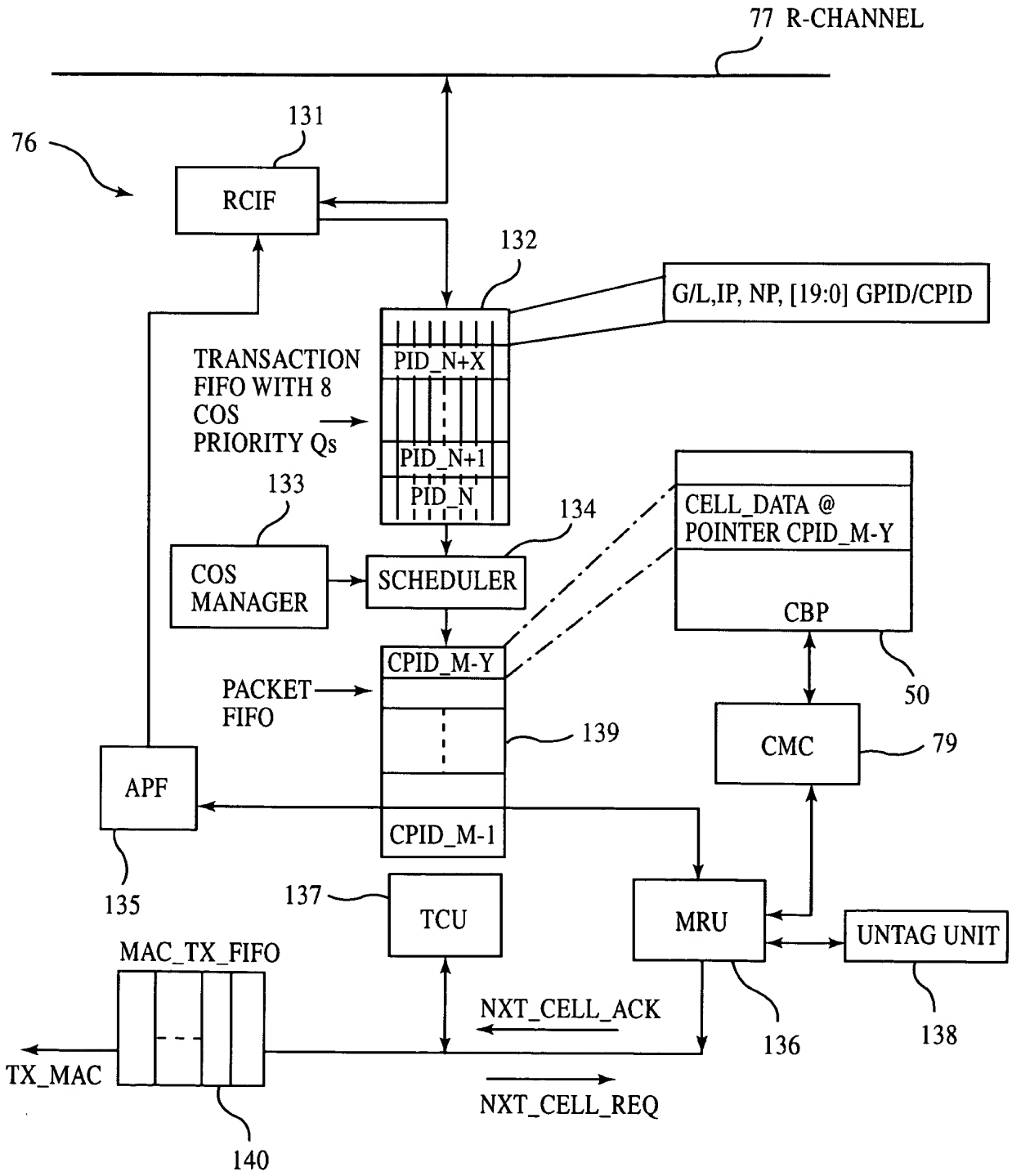

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Fig.14

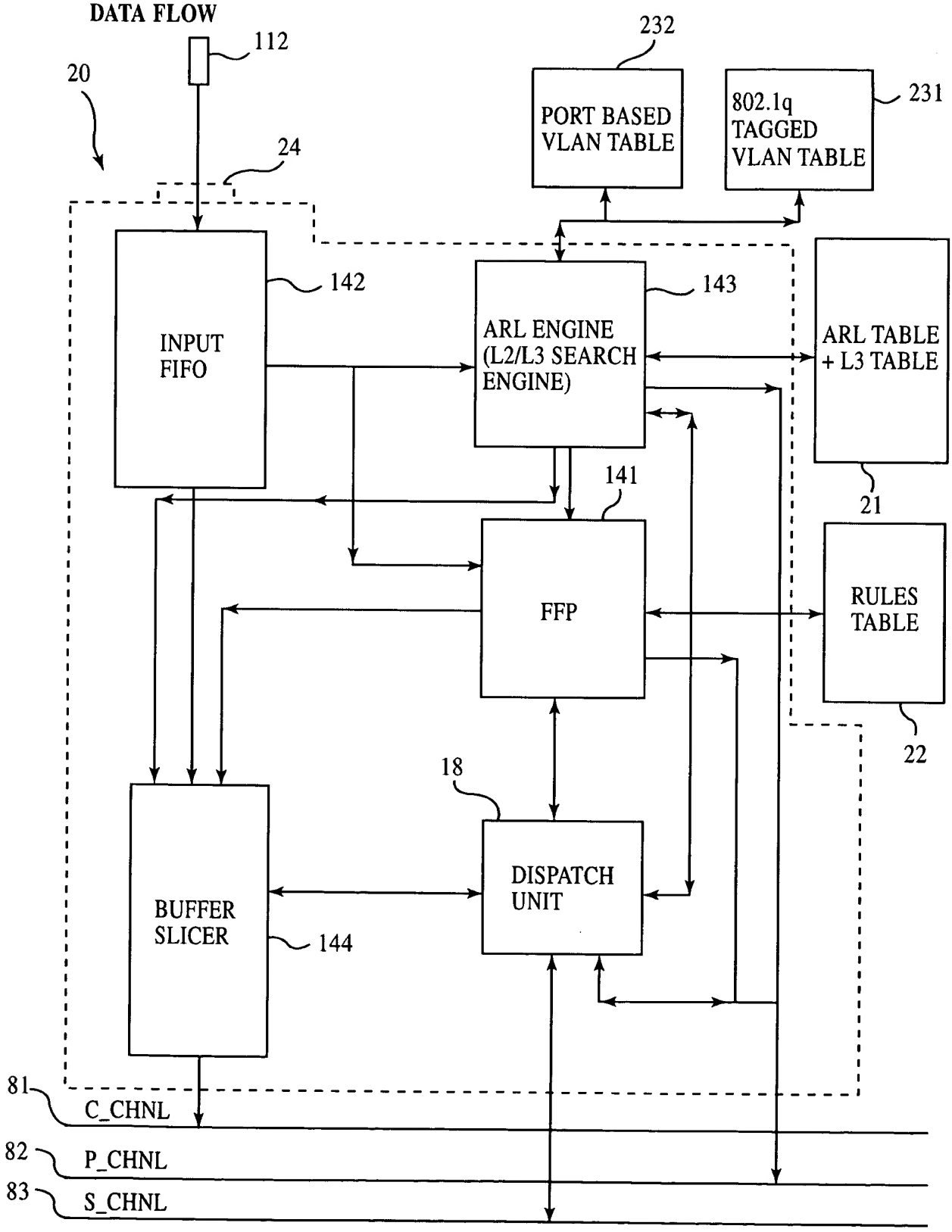

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Fig.15

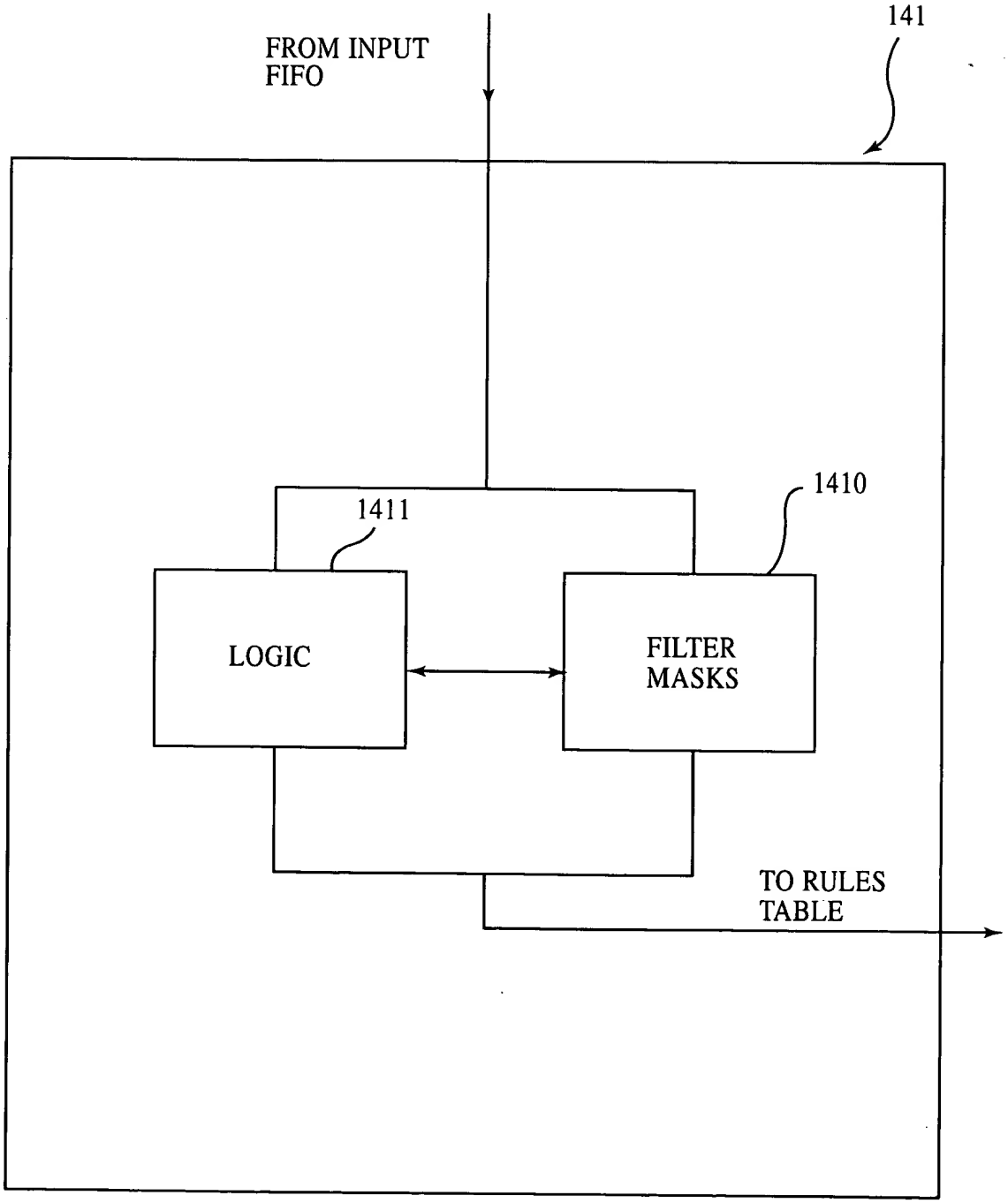

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Fig.16

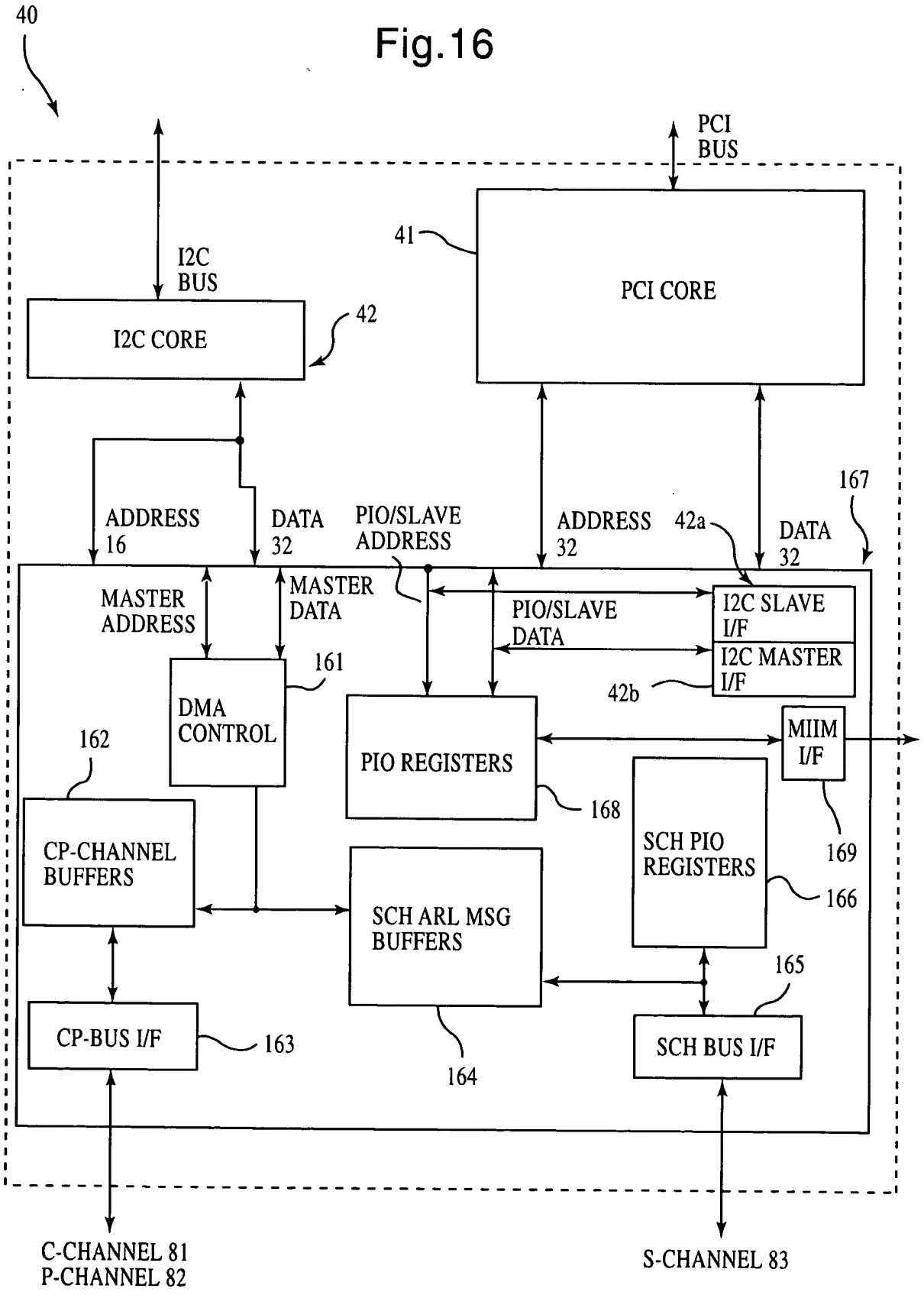

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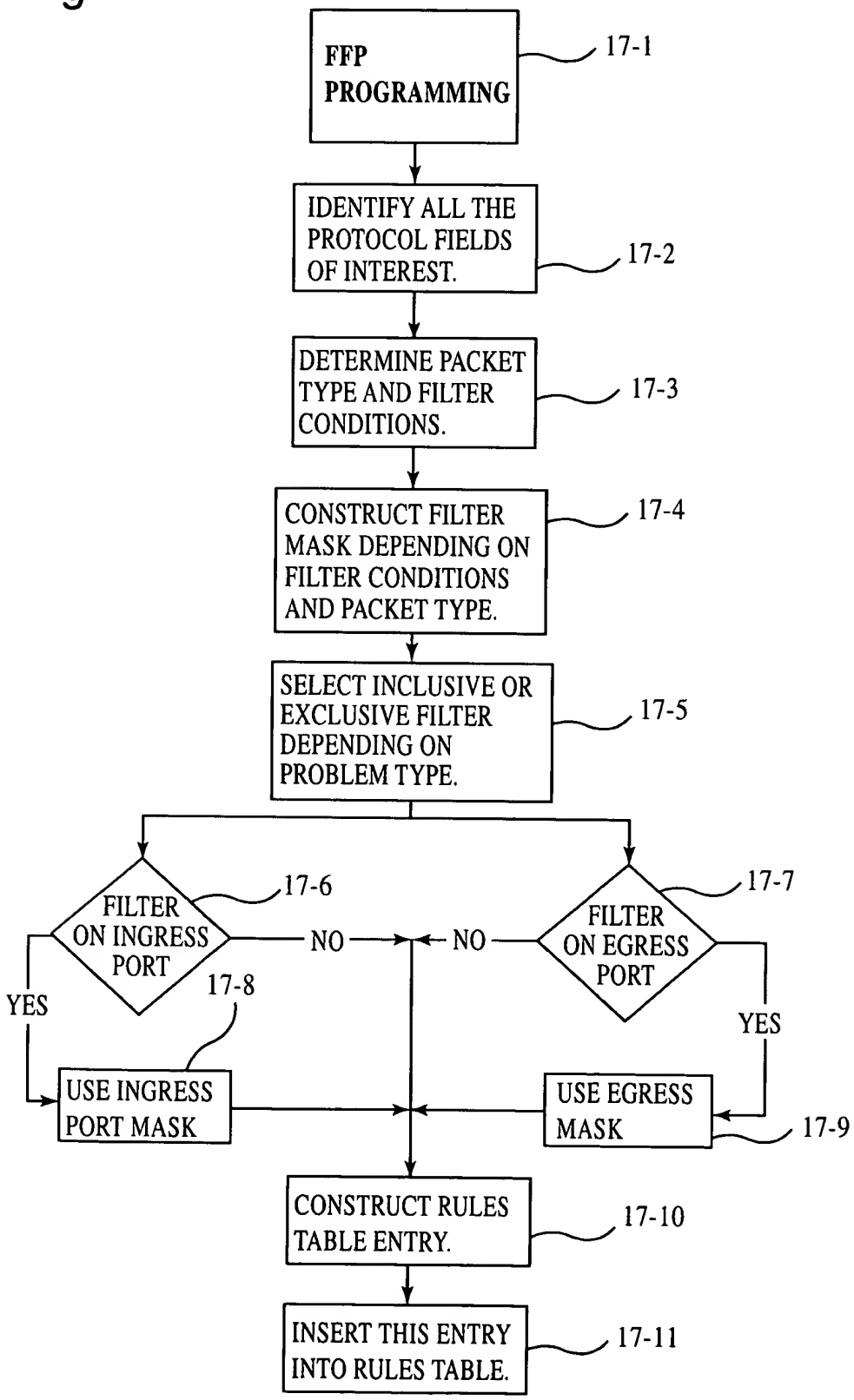
Fig.17 FFP PROGRAMMING FLOW CHART



OIPE IAP45 MAY 17 2006 PATENT & TRADEMARK OFFICE

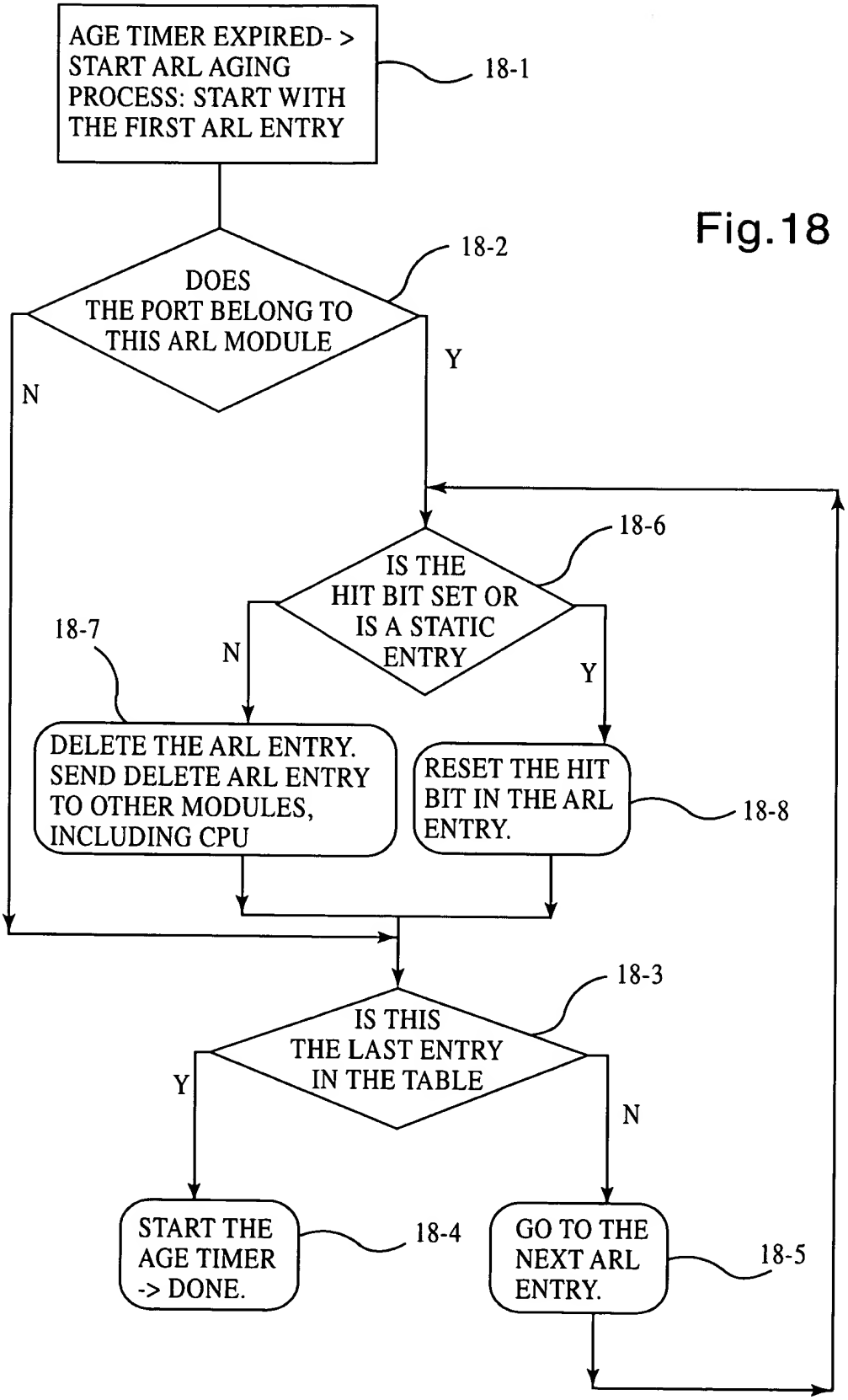


Fig.18

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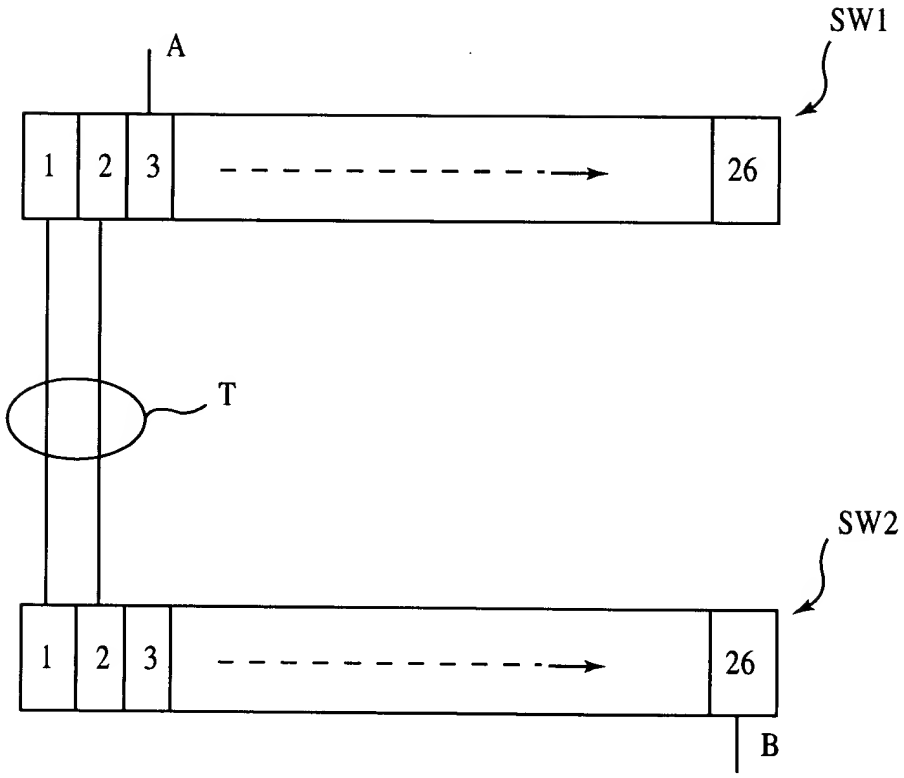

Fig.19

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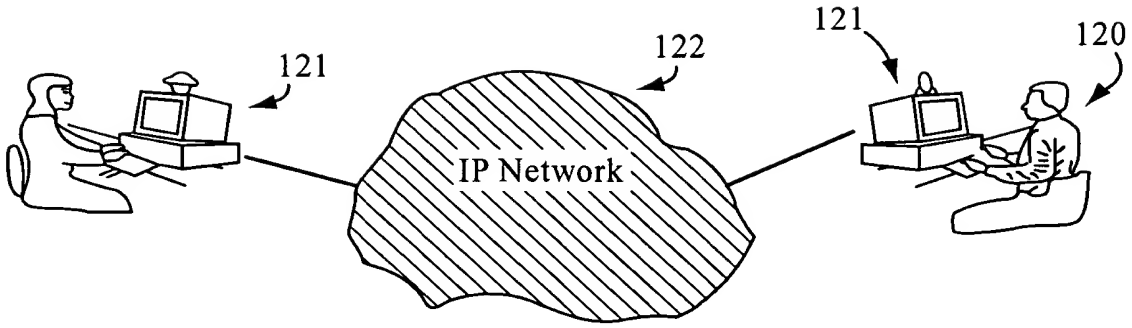

Fig.20

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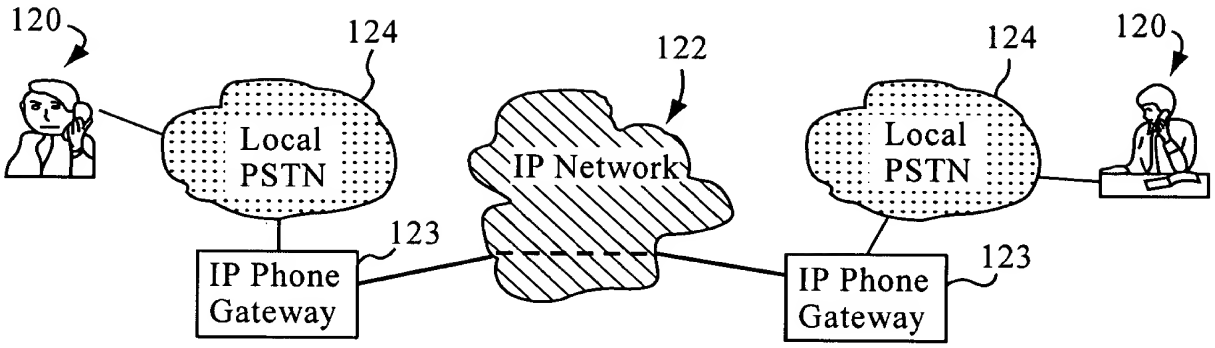

Fig.21

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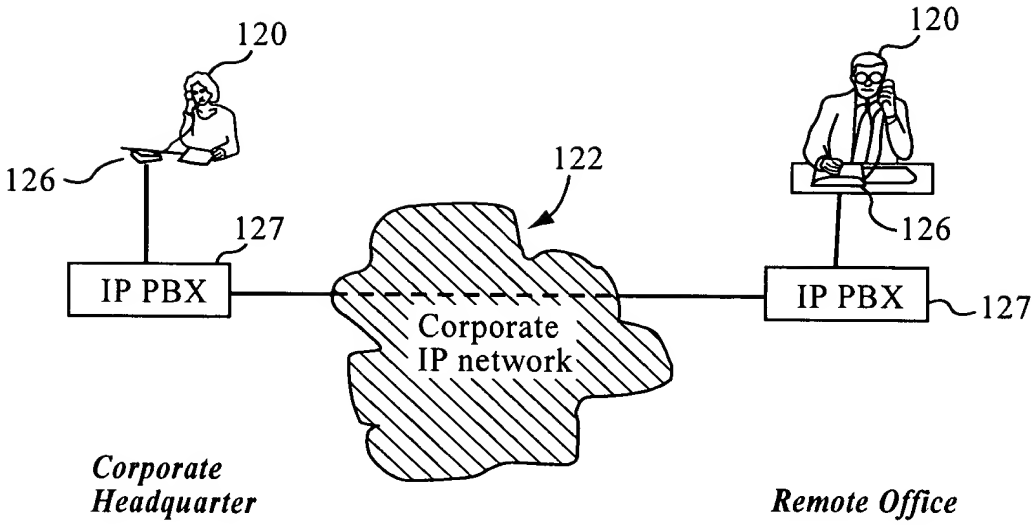

Fig.22

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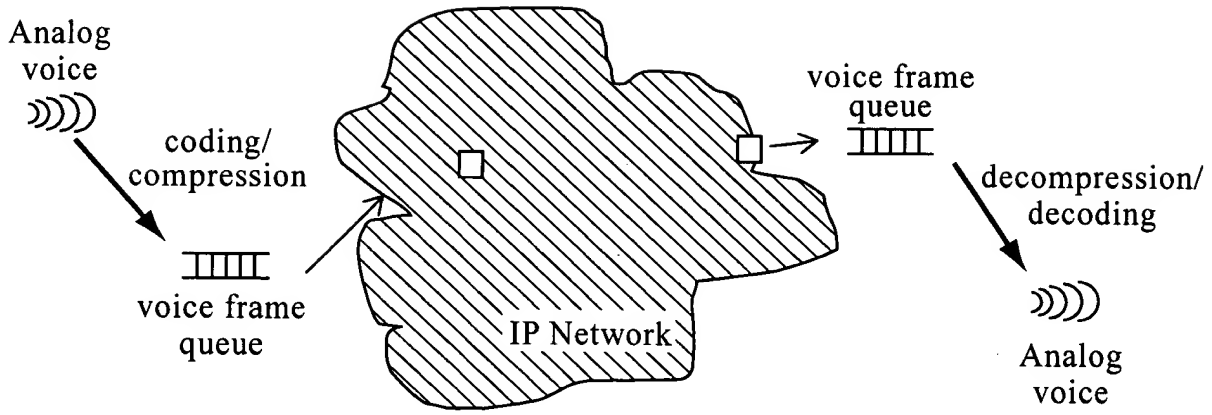

Fig.23

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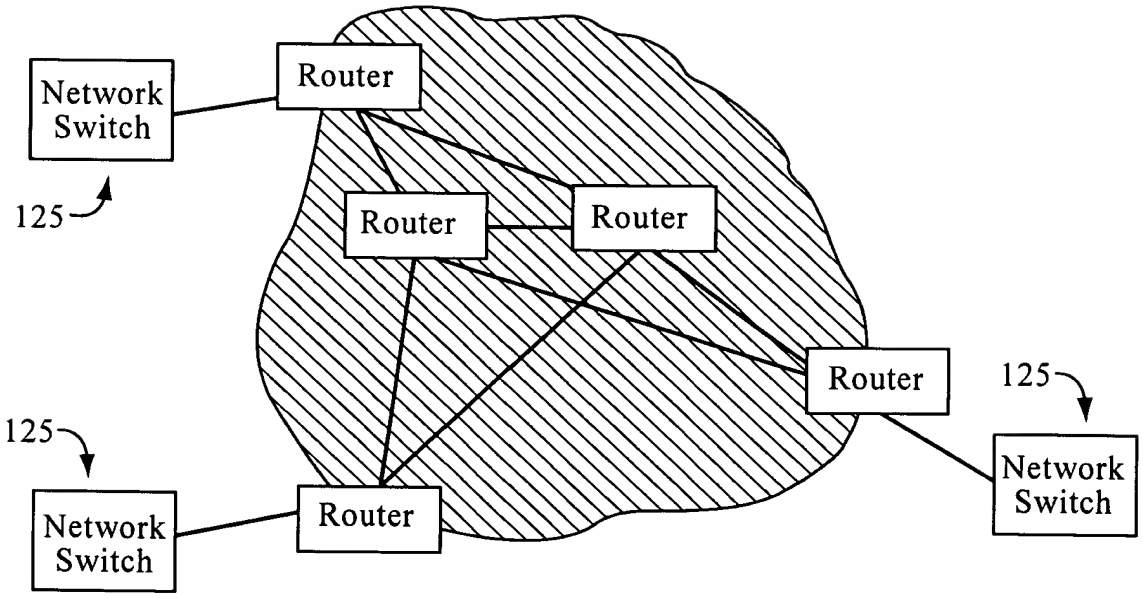

Fig.24

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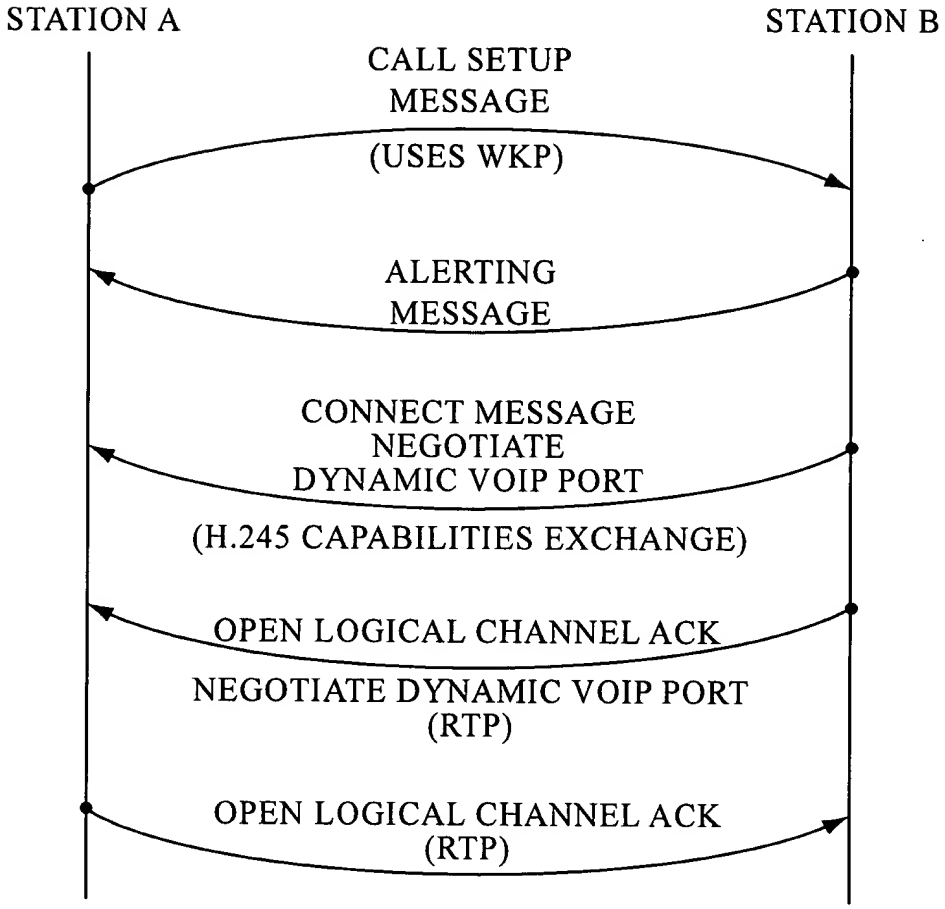

Fig.25

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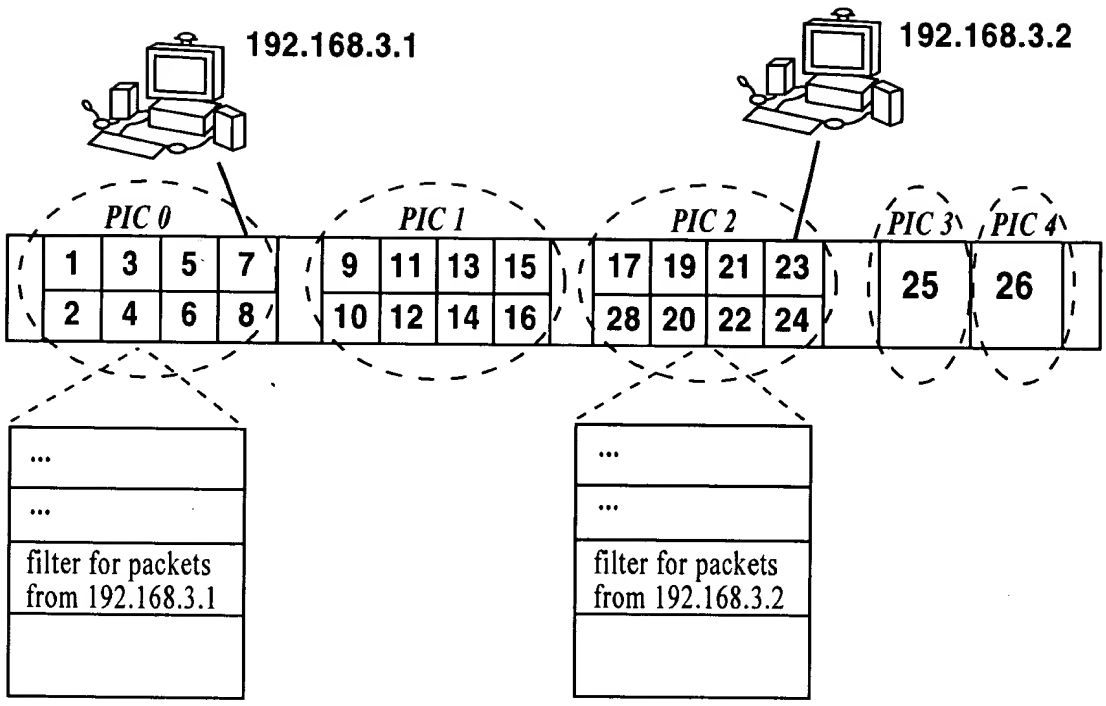

Fig.26

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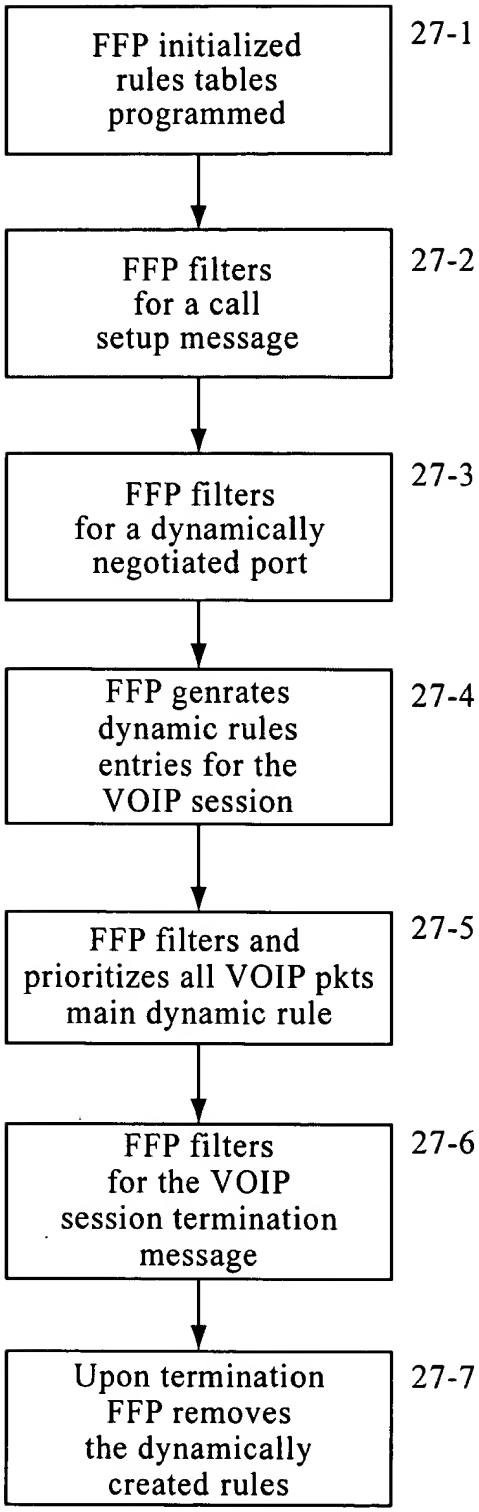

Fig.27